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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10053712	FILING DATE 01/24/2002	CLASS 438	SUBCLASS 503	GAU 2812	EXAMINER J. J. J.
<b>**APPLICANTS:</b> Yamauchi Tsunenori;					
<b>**CONTINUING DATA VERIFIED:</b> <i>None</i>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-232561 07/31/2001 <i>OK</i>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 020076	
<b>TITLE :</b> Semiconductor device and method for fabricating the same					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.    Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b> <b>Application Examiner</b>	
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